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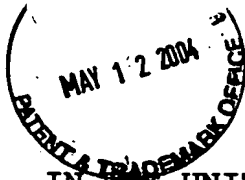
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Wen-Jian LIN

Serial No.: 10/807,129

Group Art Unit:

Filed : March 24, 2004

Examiner:

For : **AN INTERFERENCE DISPLAY CELL AND FABRICATION
METHOD THEREOF**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. 1.56 and in accordance with applicant's duty of disclosure, applicant submits a document which the Examiner may find relevant to the examination of the application. A copy of the document is enclosed, along with a listing on PTO Form 1449.

Respectfully submitted,

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May 12, 2004

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 CITATION
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 OFFICE
 TRADEMARK

Application Number	10/807,129
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Filing Date
March 24, 2004

Group Art Unit

U.S. PATENT DOCUMENTS[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.